

<b>IN THE UNITED STATES PATENT AND TRADEMARK OFFICE</b>	
In re application of: <b>Bruce B. Doris et al.</b>	Date: <b>07/17/2006</b>
Serial Number: <b>10/536,483</b>	Examiner: <b>Benjamin T. Liu</b>
Filed: <b>05/24/2005</b>	Group Art Unit: <b>2826</b>
Title: <b>Strained FinFET CMOS Device Structures</b>	IBM Corporation D/18G, B/321, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Restriction Requirement dated 06/16/2006, please elect claims as follows.